



Docket No.: 252069US2



ATTORNEYS AT LAW

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/826,391
Applicants: Hideki TAKAHASHI, et al.
Filing Date: April 19, 2004
For: INSULATED GATE BIPOLAR TRANSISTOR WITH
BUILT-IN FREEWHEELING DIODE
Group Art Unit: 2815
Examiner: LANDAU, M. C.

SIR:

Attached hereto for filing are the following papers:

REQUEST FOR RECONSIDERATION

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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Application No. 10/826,391
Reply to Office Action of April 18, 2006



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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
HIDEKI TAKAHASHI, ET AL. : EXAMINER: LANDAU, M. C.
SERIAL NO: 10/826,391 :
FILED: APRIL 19, 2004 : GROUP ART UNIT: 2815
FOR: INSULATED GATE BIPOLAR :
TRANSISTOR WITH BUILT-IN
FREEWHEELING DIODE

REQUEST FOR RECONSIDERATION

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated April 18, 2006, please amend the above-identified application as follows:

Remarks/Arguments begin on page 2 of this paper.